

Comparison between bulk micromachined and CMOS X-ray detectors

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Abstract

This paper compares two X-ray detectors fabricated using two different technologies: one is based on a bulk micromachined silicon photodetector and the other is based on a standard CMOS photodetector. The working principle of the two detectors is similar: a scintillating layer of CsI:Tl is placed above the photodetector, so the X-rays are first converted into visible light (560 nm) which is then converted into an electrical signal by the photodetector. The different aspects of the fabrication and the experimental results of both X-ray detectors are presented and discussed.

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1. Introduction

The conventional X-ray imaging remains an analog technique while other medical imaging methods such as computed tomography, ultrasound and magnetic resonance imaging are digital. Digital radiography allows application of image processing techniques (e.g. detail improvement), application of sophisticated algorithms and real-time operation. Its requirements are sub-millimeter spatial resolution and good energy resolution. The application of X-ray imaging microdetectors in medical diagnostics is undeniably advantageous. Due to their compact size, wide dynamic range and digital data storage capacity, these imagers are very promising in the medical imaging technology when combined with readout microelectronics. A significant reduction of the dose of emitted radiation can also be achieved with these X-rays microdetectors [1].

Two detectors for digital X-ray imaging are reported and compared in this article. Both are based on the same working principle: a scintillator material is placed above a photodetector. When the X-ray photons reach the scintillator, they are absorbed and converted into visible light. This visible

light is then converted into an electrical signal by the photodetector (Fig. 1). In order the visible light to achieve the photodetector and avoid cross-talk between adjacent pixels, it is necessary to have a good reflective material between them. Some approaches have been attempted by using silicon to this purpose [2,3]. The problem of these approaches is that CsI has a low refractive index (≈ 1.8) at 560 nm when compared with silicon (≈ 4). Only the light produced by the scintillator that reaches the silicon wall at low angles is reflected. The remainder light is absorbed by the silicon walls or transmitted to the adjacent pixels [4].

The first X-ray detector described in this article is based on a n^+ /p-epilayer junction placed inside a bulk micromachined cavity [5]. The cavity is then filled with a scintillating material.

The second X-ray detector is based on a CMOS standard n^+ /p-epilayer junction. The scintillating material is placed inside cavities opened in an aluminum film, which is then placed above the photodetectors [6].

2. Bulk micromachined X-ray detector

Figs. 2 and 3 show the schematic cross section, the top view and the dimensions of the device. A cavity with 2 mm \times 2 mm square size and 400 μ m depth is fabricated in a p-type

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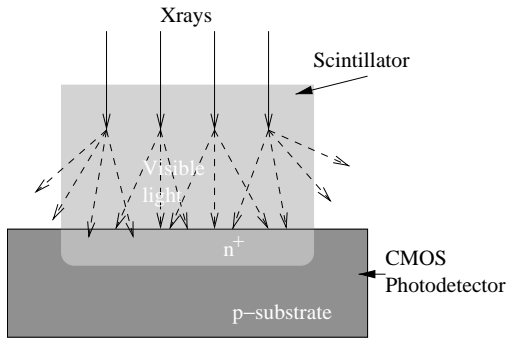


Fig. 1. Schematic design for a digital X-ray sensor: scintillator placed above a photodetector.

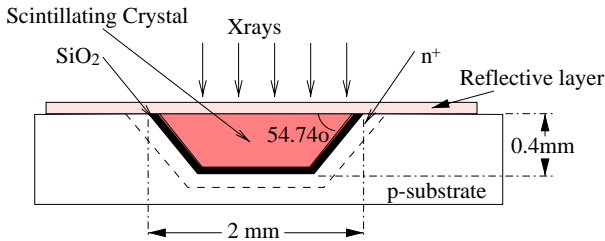


Fig. 2. Cross-section of the BMM X-ray detector.

silicon substrate using KOH etching. Inside the cavity, arsenic is implanted in order to form the n⁺/p-epilayer junction of the photodiode.

The scintillating crystal (CsI:TI) was produced by Molecular Technology GmbH, Berlin, Germany. The main parameters of this single crystal are presented in Tables 1 and 2. It was placed inside the cavity using a clamping pressure. When cut into thin sheets, cesium iodide may be bent into various shapes without fracturing, and it is reasonably soft and malleable [7]. Due to its elastic properties, a clamping pressure of 10 MPa is enough for transferring and fixing the crystal into the cavity without breaking [5]. Nevertheless this process induces some residual stresses within the material that reduces the optical transmissivity of the scintillator. So, after this step, it is necessary to anneal the scintillator in order to improve its optical transmissivity. The

Table 1
Physical properties of CsI [10–12]

Crystal class and space group	Cubic <i>Pm3m</i> (221)
Unit cell lattice parameters (Å)	4.566
Formulas per unit cell (Z)	1
Molecular weight (amu)	259.81
Density (g/cm ³)	4.53
Melting point (K)	898
Cold water solubility (g/100 g)	44.0
Elastic moduli (GPa)	18
Shear moduli (GPa)	7.3
Bulk moduli (GPa)	12.6
Poisson's ratio	0.26
Flexure strength (MPa)	5.6

Table 2
Scintillation properties of CsI:TI [13,5]

Density (g/cm ³)	4.51
Effective atomic number	54
Light yield (photons/MeV)	65900
Emission wavelength (nm)	560
Decay time (ns)	10 ³

scintillating crystal must be a good absorber of X-ray photons, but it is also desirable that the produced visible light reaches easily the surface of the photodiode. The placement of the scintillating crystal inside the cavity, by means of a clamping pressure, does not degradate the X-ray absorption, but its transmissivity for visible light is reduced. Due to this factor, the annealing process becomes necessary. The annealing is made at 340 °C during 2 h at normal atmospheric pressure [8,5]. Experimental results show that this annealing increases 12.9% the scintillator transmissivity [9].

When the X-rays reach the scintillator, the visible light is emitted randomly in all directions. In order to avoid losses of light, a reflective film above the scintillating material becomes necessary. So, as a final step, a film of reflective ink was deposited above the scintillating crystal. This film was deposited using a pressurized spray through a deposition mask.

Fig. 4 shows a picture of the device before the placement of the scintillating material and the reflective layer.

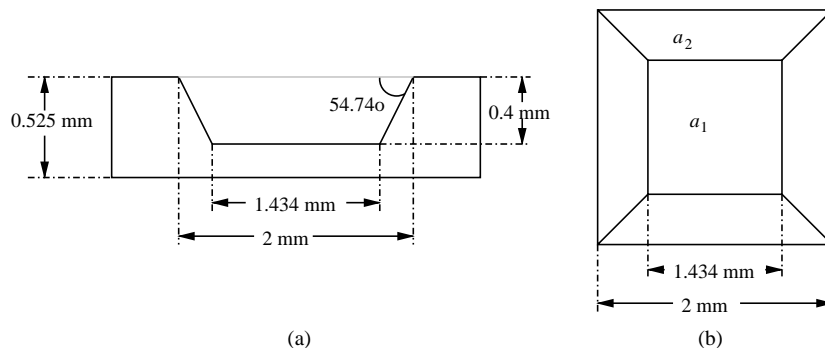


Fig. 3. Dimentions of the cavity: (a) cross-section; (b) top view. a_1 is the area of the bottom of the cavity and a_2 is the area of the lateral wall.

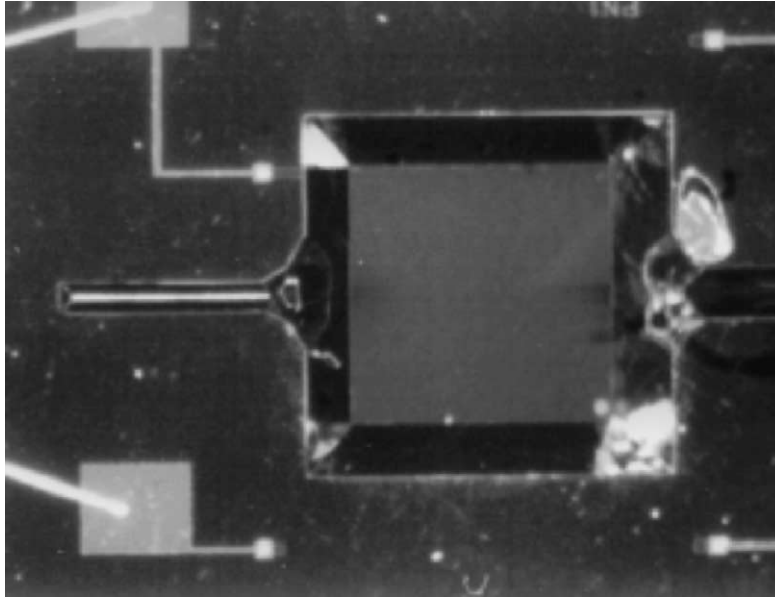


Fig. 4. Picture of the BMM X-ray detector before the placement of the CsI:Tl. The external side of the square is 2 mm.

3. CMOS X-ray detector

The second device that is described here consists in four $400\ \mu\text{m} \times 400\ \mu\text{m}$ photodiodes fabricated in a standard CMOS process. The photodiode structure chosen for this work was the n^+ /p-epilayer junction due to its highest quantum efficiency and to its spectral response in the green region of the spectrum. As it was seen in Table 2, the light yield by the scintillating crystal used in this work (CsI:Tl) is green ($\lambda = 560\ \text{nm}$). For some specific applications, other type of photodiode can be combined with scintillator materials which produce light of different colors, e.g.

$\text{Bi}_3\text{Ge}_4\text{O}_{12}$ combined with a p^+ / n -well junction for the detection of the 511 keV photons in the PET imaging.

In order to fabricate the cavities, an $800\ \mu\text{m}$ thick aluminum sheet is used. The $400\ \mu\text{m}$ diameter cavities were drilled and filled with a scintillator material by means of a clamping pressure of about 10 MPa (Figure 5). Finally the set was placed above the CMOS photodetectors [6] as is shown in Fig. 6. Fig. 7 shows a picture of the CMOS device before the placement of the aluminum dye with the scintillating material.

In this case, the walls of the cavities avoid losses of the visible light by guiding the light to the photodiode.



Fig. 5. CsI:Tl inside the aluminum holes: 2×2 array. The diameter of each hole is $400\ \mu\text{m}$.

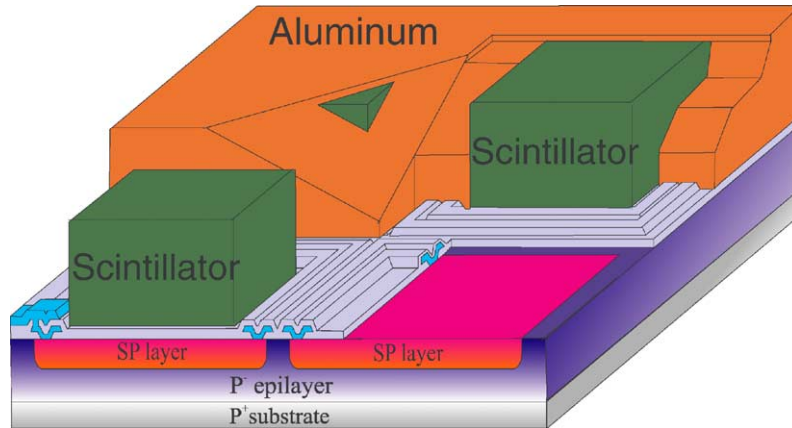


Fig. 6. Structure of a 2×2 CMOS X-ray detector array. The scintillator dimensions are $400 \mu\text{m} \times 400 \mu\text{m}$ and $790 \mu\text{m}$ high.

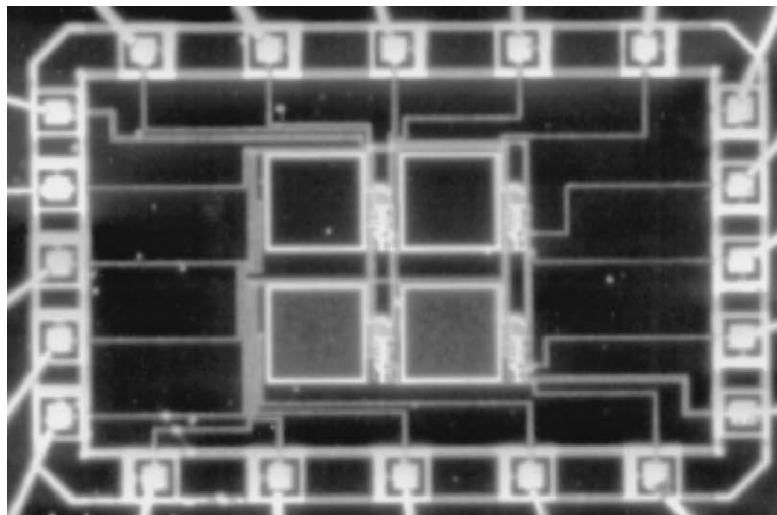


Fig. 7. Picture of the 2×2 CMOS photodetector array before the placement of the CsI:Tl layer. The size of each of the squares is $400 \mu\text{m}$.

4. Efficiency of the reflective layers

For the BMM detector, the area of the bottom of the cavity, a_1 (Fig. 3) is 2.056 mm^2 and the area of the cavity wall, a_2 is 0.859 mm^2 , so the total area of the photodetector surface is 5.492 mm^2 . The area of the scintillator surface is 9.492 mm^2 . Eq. (1) relates the light that reaches the photodiode (L_{pd}) with the one emitted by the scintillator (L_{R}) [4],

$$\frac{L_{\text{pd}}}{L_{\text{R}}} = \frac{R_{\text{A}}}{1 - (1 - R_{\text{A}})(1 - R_{\text{loss}})} \quad (1)$$

where R_{A} is the ratio between the area of the photodiode and the area of the scintillator, and R_{loss} is the percentage of losses in each reflection. In this case, $R_{\text{A}} = 5.492/9.492 = 0.579$. Assuming that the efficiency of each reflection in the mirror is 80%, $R_{\text{loss}} = 0.2$, and $L_{\text{pd}}/L_{\text{R}} = 0.873$. In the case in which the reflector does not exist, $R_{\text{loss}} = 1$ and $L_{\text{pd}}/L_{\text{R}} = 0.579$.

For the CMOS detector (Fig. 6), the area of the photodetector is 0.16 mm^2 and the area of the scintillator is 1.584 mm^2 . The ratio between both areas is $R_{\text{A}} = 0.101$.

As the reflector in this case is constituted by aluminum, its efficiency is 86%, and $R_{\text{loss}} = 0.14$. In this case, $L_{\text{pd}}/L_{\text{R}} = 0.445$. Once again, if the reflector at the top of the scintillator is removed, $L_{\text{pd}}/L_{\text{R}} = 0.322$.

5. Experimental results and comparison between the two X-ray detectors

The experiments on the two devices were performed using a didactic X-ray tube with a molybdenum anode from Leybold. In this setup, the maximum voltage of the tube is 35 kV and a current can be changed from 0 mA to 1 mA. Under these experimental conditions, the tube produces X-rays with an energy peak near 20 keV. The results of these measurements are shown in Fig. 8 for the BMM detector and in Fig. 9 for the CMOS detector.

- The BMM detector do not use a standard fabrication process in the construction of the photodetector. This fact allows the optimization of the junction depth in order to

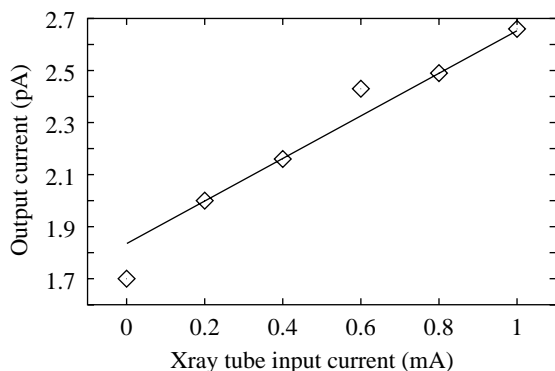


Fig. 8. Output current of the BMM X-ray detector with a X-ray tube input voltage of 35 kV.

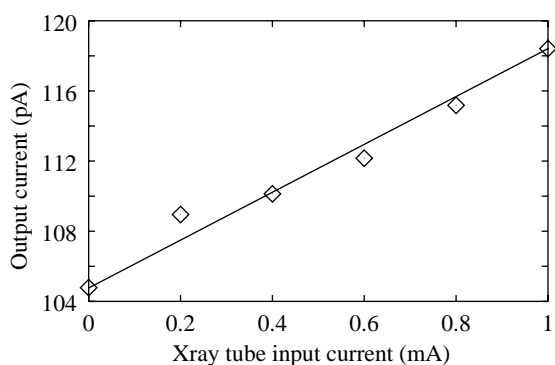


Fig. 9. Output current of the CMOS X-ray detector with a X-ray tube input voltage of 35 kV.

obtain a spectral response that matches the emission peak of the scintillator. Nevertheless, the fabrication of the CMOS detector is cheaper as it make use of a standard fabrication process.

- The fabrication of the cavities in the BMM detector is easier due to the use of anisotropic chemical etching of the silicon instead of the mechanical methods used for the CMOS detector.
- The dimensions of the pixels in the CMOS detectors can be smaller than in the BMM detector due to the fact that the side walls of the cavities fabricated with KOH etching in the BMM detector are not vertical (Fig. 2).
- For the applications of these detectors for digital radiography, a pixel size of about 200 μm is required. With the BMM technology this limit is very difficult to achieve, whereas CMOS detectors with dimensions below 200 μm are easily achieved.
- Due to the standard fabrication of the CMOS prototype, it is possible to integrate the electronics with the photodetectors without additional fabrication steps. This integration is more difficult in the BMM detector.
- The BMM structure is more efficient than the CMOS detector in terms of light emitted by the scintillator that reaches the photodetector (Section 4).

- Both detector prototypes show a linear response up to 1 mA of X-ray tube input current (Figs. 8 and 9).
- The CMOS detector has a higher sensibility, but a larger offset (Fig. 9).

6. Conclusion

Two X-ray detector prototypes for digital radiography were fabricated using two different technologies: one is based on a bulk micromachined silicon photodetector and other is based on a standard CMOS photodetector. The fabrication process and the performance of the two prototypes were presented and discussed comparatively. Whereas the CMOS detector has advantages in the integration of the photodetectors with the electronics and the detection sensibility, the BMM detector shows advantages in the fabrication of the cavities and the lower offset. Miniaturization down to the limit of 200 μm , which is necessary for practical purposes, is easier to achieve for the CMOS detectors. Further, even when the tests were performed for X-rays energy of 20 keV, larger energies are more easily detected by this kind of devices.

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Biographies

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R.F. Wolfenbuttel received the MSc and PhD degrees from the Delft University of Technology, Delft, The Netherlands, in 1984 and 1988, respectively. Between 1986 and 1993, he was an assistant professor, and since 1993, an associate professor, at the Department of Microelectronics of the Delft University of Technology, where he is involved in instrumentation and measurement in general and on-chip functional integration of microelectronic circuits and silicon sensor, fabrication compatibility issues, and micromachining in silicon and microsystems, in particular. He was a visitor at the University of Michigan, Ann Arbor, in 1992, 1999, and 2001, at Tohoku University, Sendai, Japan, in 1995, and at EPFL, Lausanne, Switzerland, in 1997. Dr. Wolfenbuttel was the recipient of a 1997 NWO pioneer award. He served as general chairman of the Dutch National Sensor Conference in 1996, Eurosensors in 1999 and The MME workshop in 2003.

J.H. Correia graduated in physical engineering at University of Coimbra, Portugal in 1990. He obtained his PAPCC (equivalent to MSc degree) in 1994 at University of Minho, Portugal and his PhD degree in 1999 at Delft University of Technology, The Netherlands. He is an associate professor at the Department of Industrial Electronics of the University of Minho and he is involved in instrumentation and measurement in general and on-chip functional integration of microelectronic circuits and silicon sensors. He served as general chairman of the Eurosensors conference in 2003.